

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,975,003 B2
DATED : December 13, 2005
INVENTOR(S) : Osamu Matsui et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page, Item [54] and Column 1, line 1,

Title, change "SEMICONDUCTOR CMOS SOI" to -- SEMICONDUCTOR
DEVICE AND METHOD FOR FABRICATING THE SAME --;

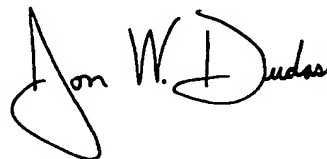
Title page,

Item [56], **References Cited**, insert the following:

-- FOREIGN PATENT DOCUMENTS
3111947 9/2000 (JP) --.

Signed and Sealed this

Twenty-third Day of May, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office